

Docket No. 214391US2S

IN RE APPLICATION OF: Hidetaka HATTORI

SERIAL NO: 09/961,248

FILED: September 25, 2001

FOR: POWER SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 2814

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

- ☒ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☒ Additional documents filed herewith: Marked-Up Copy

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The Fee has been calculated as shown below:

| CLAIMS | CLAIMS REMAINING | | HIGHEST NUMBER PREVIOUSLY PAID | NO. EXTRA CLAIMS | RATE | CALCULATIONS |
|-------------|---------------------|--|---|------------------------|-----------|--------------|
| TOTAL | 40 | MINUS | 40 | 0 | x \$18 = | \$0.00 |
| INDEPENDENT | 4 | MINUS | 4 | 0 | x \$84 = | \$0.00 |
| | | <input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS | | | + \$280 = | \$0.00 |
| | | TOTAL OF ABOVE CALCULATIONS | | | | \$0.00 |
| | | <input type="checkbox"/> Reduction by 50% for filing by Small Entity | | | | \$0.00 |
| | | <input type="checkbox"/> Recordation of Assignment | | | + \$40 = | \$0.00 |
| | | TOTAL | | | | \$0.00 |

☐ A check in the amount of \$0.00 is attached.

- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.



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Customer Number 22850
Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 10/01)
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OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Michael R. Casey

Gregory J. Maier

Registration No. 25,599

Michael R. Casey, Ph.D.

Registration No. 40,294

214391US-2 S

"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 2814"

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
HIDETAKA HATTORI : EXAMINER: FARAHANI, D.
SERIAL NO: 09/961,284 :
FILED: SEPTEMBER 25, 2001 : GROUP ART UNIT: 2814
FOR: POWER SEMICONDUCTOR :
DEVICE AND MANUFACTURING
METHOD THEREOF

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TECHNOLOGY CENTER 2800
Mr. Brownson
5/18/03

enter
the Amendment
of

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action dated August 27, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend Claims 1-40 as shown in clean form below.¹

1. (Three Times Amended) A power semiconductor device comprising:
a base layer of a first conductivity type;
a base layer of a second conductivity type selectively formed on one surface of said
base layer of the first conductivity type;

¹A marked-up copy of the amended portion of the claims is attached hereto.